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34313 7590 05/04/2004

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IRVINE, CA 92614-2558
07/13/2004 HLE444 00000097 10040122

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Jeffrey A. Miller

(Depositor's name)

(Signature)

JULY 13, 2004

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/040,122	01/02/2002	David G. Scott	FIS9-2001-0396US1	8789

TITLE OF INVENTION: METHOD FOR IDENTIFYING TEST POINTS TO OPTIMIZE THE TESTING OF INTEGRATED CIRCUITS USING A GENETIC ALGORITHM

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	08/04/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
GARBOWSKI, LEIGH M	2825	716-004000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

Orrick, Herrington & Sutcliffe

1 _____

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Cadence Design Systems, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, California

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

4a. The following fee(s) are enclosed:

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JUL 12 2004

Patent
700693-4012

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:)
David G. Scott) **Group Art Unit: 2825**
Application No.: 10/040,122)
Filed: January 2, 2002)
For: Method For Identifying Test Points to)
Optimize The Testing of Integrated Circuits)
Using A Genetic Algorithm)

TRANSMITTAL LETTER

Commissioner For Patents
P.O. Box 1450
Alexandria VA 22313-1450
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Sir:

In response to the Notice of Allowance mailed by the United States Patent and Trademark Office on May 4, 2004, enclosed for filing in the above-identified application are the following:

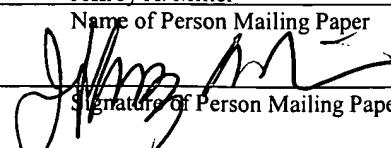
DOCUMENTS ENCLOSED:

- Fee(s) Transmittal (PTO form PTOL-85)
- Check No. 584784 in the amount of \$1630.00 to cover the Issue and Publication Fee.

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(37 C.F.R. §1.8a)

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July 8, 2004
Date of Deposit

Jeffrey A. Miller
Name of Person Mailing Paper

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Patent
700693-4012

Postcard for Return Receipt.

The Commissioner is authorized to charge Deposit Account No. 15-0665 for any fees that may be associated with this communication and are not covered in whole or in part by the enclosed check.

Respectfully submitted,

Orrick, Herrington & Sutcliffe LLP

Dated: July 8, 2004

By:


Jeffrey A. Miller
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